

| FACULTY | AY | DETAILS |
|--------------------|-------|---|
| DR.P.M.K.Prasad | 18-19 | P.M.K.Prasad, G.Sasibhushan Rao, M.N.V.S.S.Kumar and K.Chiranjeevi , "Design and implementation of non-perfect reconstruction biorthogonal wavelets for edge detection of X-ray images" |
| D.V.A.N.Ravi Kumar | 18-19 | 1. D.V.A.N.Ravi Kumar, S Koteswara Rao, K Padma Raju,"A novel estimation algorithm for torpedo tracking in undersea environment", Journal of central south university, 26(3),673-683,2019 |
| D.V.A.N.Ravi Kumar | 17-18 | 1. D.V.A.N.Ravi Kumar, S Koteswara Rao, K Padma Raju, Estimate-Merge-Technique-based algorithms to track an underwater moving target using towed array bearing-only measurements, Sadhana, 42(9): 1617- 1628, 2017. |
| D.V.A.N.Ravi Kumar | 17-18 | 2. D.V.A.N.Ravi Kumar, S Koteswara Rao, K Padma Raju, Performance evaluation of a novel hybrid stochastic algorithm while tracing an underwater moving target, International Journal of Pure and Applied Mathematics, 117(18):373-378, 2017. |
| CH.Sirisha | 17-18 | 1. R.Roopa Sree, Ch.Sirisha, Low Power and Resource Efficient IFO Estimation with High Speed Architecture, International Journal of Applied Sciences, Engineering and Management, 6(4): 23-29, July 2017.JC17-18(2) |
| M.Mani Kumari | 17-18 | V Lakshmi Mounika, M.Mani Kumari, Dynamic feedback control 10T SRAM Cell, International Journal of Applied sciences, Engineering and Management, 6(4): 17-22, July 2017. JC17-18(1) |
| B.Vijaya Lakshmi | 16-17 | 1. B.Vijayalakshmi, P.Siddaiah, Cooperative Spectrum sensing technique based on blind detection method, Internatioanl Journal of Advanced Engineering, Management and Science, 2(10):1772-1776, 2016. |
| D.V.A.N.Ravi Kumar | 16-17 | 1. D.V.A.N. Ravi Kumar, S.Koteswara Rao, K.Padma Raju, A Novel Stochastic estimator using Pre-processing technique for long-range target tracing in heavy noise environment, OPTIK, 127(10):4520-30, 2016.JC16-17(10) |
| D.V.A.N.Ravi Kumar | 16-17 | 2. D.V.A.N.Ravi Kumar, S.Koteswara Rao, K.Padma Raju, Integrated Unscented Kalman filter for underwater passive target tracing with towed array measurements, OPTIK, 127(5):2840-47, 2016.JC16-17(11) |
| CH.Sirisha | 16-17 | 1. Ch.Sirisha, Navya N, A Probably Secure Arm Based Health Monitoring System Through Hybrid Cryptography Using Embedded System, International Journal Of Advance Research In Electronics and Communication Engineering, 5(9):2286-2292, 2016.JC16-17(9) |
| M.Mani Kumari | 16-17 | M. Mani Kumari, "Boostable Repeater Design with Dual Adaptive Supply Voltage for VLSI Interconnects", International Journal of Advanced Engineering and Research Development, 2(11), 2015. |
| P.V.K.Chaitanya | 16-17 | N. Neeraja, PVK Chaitanya , KCB Rao, Design And Fabrication Of S-Band MIC Power Amplifier, International Journal On Cybernetics & Informatics, 5(4): 55-64,2016.JC16-17(5) |

| | | |
|--------------------|-------|---|
| I.Lavanya | 16-17 | 1. Sushma D, I.Lavanya, Low Voltage High Accurate Current Mode Analog Multiplier, International Journal of Computer Engineering In Research Trends, 3(8):447-453, 2016.JC16-17(2) |
| R.Jalaja | 16-17 | M.Charita, S.Manisha, R.Ramayasai, S.R.Ramani and R.Jalaja, "Contineous Motorcycle Handle control monitoring system" MAY2017.JC16-17(1) |
| G.P.S.Prasanthi | 16-17 | 1. G.P.S Prashanti, N. navya Sirisha, N.Akhila Reddy, Low Power Adiabatic Logic Design, IOSR Journal of Electronics and Communication Engineering, 12(1): 28-34, 2017.JC16-17(7) |
| G.P.S.Prasanthi | 16-17 | 2. Moulika Devi V, G.P.S Prasanthi, Analog and Digital PLL with Single Ended Ring VCO for "Full Swing Symmetrical Even Phase Outputs", International Journal of Computer Engineering In Research Trends, 3(8):441-446, 2016. JC16-17(6) |
| B.Vijaya Lakshmi | 15-16 | 1.Dynamic spectrum allocation using Genetic algorithms” National Conference on Communications, Antennas and Signal processing - Proceedings published in ANU Journal of Engineering & technology (ISSN: 0976-3414). |
| B.Vijaya Lakshmi | 15-16 | 2. B.Vijayalakshmi, 5G Mobile technology: A Survey, Journal of Wireless Communication, Networks and Mobile Engineering Technology, 1(3):20-30, 2016. |
| D.V.A.N.Ravi Kumar | 15-16 | 1. D.V.A.N. Ravi kumar, S.Koteswara Rao, K.Padma Raju “Underwater Bearings-only passive Tangent Tracing using Estimate fusion Technique”, Advances in Military Technology, 10(2), pp :31-44, 2015.JC15-16(7) |
| CH.Sirisha | 15-16 | 1. A.Nagalakshmi and Ch.Sirisha, “Performance optimization of dynamic and domino logic carry lookahead adder using CNTFET in 32nm Technology”, IOSR.J. VLSI & Sig Processing, 5(5), pp: 1-9, 2015.JC15-16(3) |
| P.V.K.Chaitanya | 15-16 | K.C.B.Rao, T.S.Prasad and P.V.K.Chaitanya, "Fused Kalman Filter for a constant turn Radar", International Journal of emerging technology and advanced engineering",6(2), pp.72-77, Feb.2016. |
| G.P.S.Prasanthi | 15-16 | 2. G.P.S.Prasanthi, K.Sirisha, G.Ramya, B. Padma, Speech to Text Conversion Using HMM, International Journal of Advanced Research in Electronics and Communication Engineering, 5(3):557-560, 2016JC15-16(4) |
| DR.K.Srinivasa Rao | 14-15 | K.Srinivasa Rao and B.Prabhakar rao, "Non-Linear Companding transform aided MIMO-OFDM systems",9(5) ver.2, pp.22-30,sep.2014. |
| D.V.A.N.Ravi Kumar | 14-15 | G.N.Durga and D.V.A.N.Ravi kumar, "Gdi Technique based carry look ahead adder design" ,IOSR Journal of VLSI and Signal Processing(IOSR-JVSP),4(6),PP.1-9,Nov.2014 |
| M.Mani Kumari | 14-15 | R.S.G.Bhavani, M.Manikumari and K.Padma Priya, "Low Power and High Performance VLSI Interconnects by schmitt trigger technique in nanoscale regime", 4(5) ver.2,pp.40-47, sep.2014. |

| | | |
|--------------------|--------|---|
| N.V.Maheswara Rao | 14-15 | N.V.Maheswara Rao, "Low power design of Schmitt trigger based SRAM cell using NBTI technique", International Journal of advanced research in electrical, electronics and instrumentation engineering, 3(10), pp.16664-16670, oct. 2014. |
| B.Lakshmi | 14-15 | K.V.Lakshmi and B.Lakshmi, "Power efficient and noise immune domino logic for wide fan in gates", 3(10), pp.12692-12699, oct.2014. |
| K.Sreelakshmi | 14-15 | V.Jyothi and K.Sreelakshmi, "Realization of Low power and High speed parallel prefix adders", International Journal of scientific engineering and technology research, 3(31), pp.6147-6152, oct.2014. |
| K.Sreelakshmi | 14-15 | K.Sreelakshmi, D.Hari Priya, K.L.P.Rao and B.V.Priyanka and D.Geetha, "Design of aperture coupled rectangular microstrip patch antenna to operate in S-Band at 2.25 GHz", International Journal for scientific research & development, 2(12), pp.617-620, 2015. |
| DR.K.Srinivasa Rao | 13-14 | K.Srinivasa Rao and B.Prabhakar Rao, "PAPR Analysis by adaptive active constellation extension for STBC MIMO-OFDM Systems", International Journal of advanced trends in computer science and engineering, 2(5), pp.59-63, 2013. |
| D.V.A.N.Ravi Kumar | 13-14 | M.S.Lakshmi, M.Sujatha and D.V.A.N.Ravi Kumar, "Design and Development of FPGA based SD card Interfacing using Soft-Core Processor", 1(4), pp.23-26, sep.2013. |
| D.V.A.N.Ravi Kumar | 13-14 | D.V.A.N.Ravi Kumar, S.Koteswara Rao and K.Padma Raju, "Underwater Active Target Tracking using Kalaman Filter", International Journal of engineering research & Technology, 2(10), pp.3982-3988, oct.2013. |
| M.Mani Kumari | 13-14 | M.V.M.Lahari and M.Mani Kumari, "Bit swapping LFSR and its application to fault detection and diagnosis using FPGA", International journal of engineering research & technology, 2(9), pp.1204-1208, sep.2013. |
| N.V.Maheswara Rao | 13-14 | G.Lavanya, S.Maitra and N.V.Maheswara Rao, "LLSI Architecture for pipelined lifting based 2-D DWT with booth multiplier", International Journal of engineering research & Technology, 2(11), pp.610-614, nov.2013. |
| L.Sarika | 13-14 | CH.Haritha and L.Sarika, "Design of CMOS Full adder cells for arithmetic applications", International Journal of engineering & Technology, 2(9), pp.1033-1036, sep.2013 |
| I.Lavanya | 13-14 | N.Lakshmi, P.S.Prasad, I.Lavanya and M.Nirmala, "Development and Testing of an Embedded Vibration monitoring system", International Journal of electronics & Communication Engineering research, 1(3), pp.93-96, aug.2013 |
| R.Jalaja | 13-14 | B.Biswal, S.Mishra and J.Jalala, "Automatic classification of power quality events using balanced neural tree", IEEE Transactions on Industrial Electronics, 61(1), pp.521-521-530, jan.2014 |
| K.Sreelakshmi | 12-13. | K.Sreelakshmi and A.Srinivasa Rao, "An advanced and area optimized L.U.T design using A.P.C and O.M.S", International journal of computer science and information technologies, 3(3), 4265-4269, 2012 |