

GAYATRI VIDYA PARISHAD

COLLEGE OF ENGINEERING WOMEN

Department OF ELECTRONICS AND COMMUNICATION ENGINEERING

COURSE PLAN

Subject Code	Name of the Subject	Class/ Semester	Name of the Faculty/ Designation	No. of Students	Total periods per Semester	
					Lectures	Tutorials
	Microprocessors & Microcontrollers	III / IV ECE-A	Dr.L.Ganesh Assistant professor	63		

15-01-2018 to 21-01-2018 mid-1

19-03-2018 to 24-03-2018 mid-2

Week No.	Lecture No.	Topic	Date on which to be completed*
W-1	1	Outcome based Education, Course Objectives, Course outcomes	20-11-2017
	2	Unit-i: 8086/8088 microprocessors -introduction	20-11-2017
	3	Register organization of 8086	22-11-2017
	4	Architecture, signal description of 8086	23-11-2017
	5	Physical memory organization	25-11-2017
W-2	6	General bus operation, I/O addressing capability	27-11-2017
	7	Special purpose activities	27-11-2017
	8	Minimum mode, maximum mode of 8086 system and timings	29-11-2017
	9	The processor 8088	30-11-2017
W-3	10	Machine language instruction formats	04-12-2017
	11	Addressing mode of 8086	04-12-2017
	12	Instruction set of 8086	06-12-2017
	13	Assembler directives and operator	07-12-2017
W-4	14	Unit-ii: programming with 8086 microprocessor —introduction	11-12-2017
	15	Machine level programs	11-12-2017
	16	Programming with an assembler	13-12-2017
	17	Assembly language programs	14-12-2017
	18	Introduction to stack, stack structure of 8086/8088	16-12-2017
W-5	19	Interrupts and interrupt service routines	18-12-2017
	20	Interrupt cycle of 8086,	18-12-2017
	21	Non-mask able interrupt	20-12-2017
	22	Mask able interrupts	21-12-2017
	23	Interrupt programming	23-12-2017
W-6	24	Unit-iii: basic and special purpose programmable peripherals and their interfacing with 8086/88 -introduction	27-12-2017
	25	Semiconductor memory interfacing	28-12-2017
	26	Dynamic RAM interfacing	30-12-2017
W-7	27	Interfacing i/o ports,	01-01-2018
	28	PIC 8255 modes of operation of 8255	01-01-2018
	29	Interfacing to D/A and A/D converters,	03-01-2018
	30	Stepper motor interfacing	04-01-2018
	31	Control of high power devices using 8255.	06-01-2018
W-8	32	Programmable interrupt controller 8259A	08-01-2018
	33	The keyboard /display controller 8279	08-01-2018

	34	Programmable communication interface 8251 USART	10-01-2018
	35	Dma controller 8257	11-01-2018
W-9	36	Discussion on mid-1 Question paper	22-01-2018
	37	Unit-iv: advanced micro processors Introduction	22-01-2018
	38	Salient features of 0386DX	24-01-2018
	39	Architecture of 80386	25-01-2018
	40	Signal description of 80386	27-01-2018
W-10	41	Register organization of 80386	29-01-2018
	42	Addressing modes,	29-01-2018
	43	Data types of 80386	31-01-2018
	44	Real address mode of 80386	01-02-2018
	45	Protected mode of 80386,	03-02-2018
W-11	46	Segmentation and Paging,	05-02-2018
	47	Virtual 8086 mode and	05-02-2018
	48	Enhanced mode	07-02-2018
	49	Instruction set of 80386	08-02-2018
W-12	50	The coprocessor 80387.	12-02-2018
	51	Revision	12-02-2018
	52	Unit-v: 8051 microcontroller Introduction to microcontrollers	14-02-2018
	53	8051Microcontrollers	15-02-2018
	54	8051pin description, connections	17-02-2018
W-13	55	I/O ports and	19-02-2018
	56	Memory organization	19-02-2018
	57	MCS51addressing modes	21-02-2018
	58	Instructions	22-02-2018
	59	Assembly language programming tools.	24-02-2018
W-14	60	Revision	26-02-2018
	61	Unit-vi: pic microcontrollers and arm 32-bit Microcontroller-introduction	26-02-2018
	62	Overview and features	28-02-2018
	63	PIC16Cx/7X instructions,	01-03-2017
	64	Interrupts in PIC 16C61/71	03-03-2017
W-15	65	PIC 16F8XX Flash controllers	05-03-2017
	66	I/O ports	05-03-2017
	67	Timers	07-03-2017
	68	Introduction to 16/32 Bit processors,	08-03-2017
W-16	69	ARM architecture	12-03-2017
	70	ARM organization,	12-03-2017
	71	Arm / thumb Programming model,	14-03-2017
	72	ARM / Thumb instruction set	15-03-2017
	57	Revision	17-03-2017

Signature of the Faculty Member
Date:

Signature of the HOD
Date: