



II B.Tech(CSE)-CO-UNIT-I- MCQs

MULTIPLE CHOICE QUESTIONS

1)The major Functional Components of a Computer System are:

CPU (ii) Memory (iii) ALU (iv) EU (v) I/O (vi) CU
(i) (ii) (v) (b) (i) (ii) (iii) (c) (i) (ii) (iii) (v) (d) (i) (ii) (vi)

ANS:(a)

2)Storage of data is done in

RAM (b) Registers (c) Cache (d) All

ANS: (d)

3)CPU consists of the following Functional Units:

CU (b) ALU (c) Both (d) None

ANS: (c)

4)Bus is collection of _____.

Lines (b) Signals (c) Both (d) None

ANS: (a)



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5) System Bus connects the following components:

(i) CPU (ii) Memory (iii) CU (iv) I/O (v) Cache

(i) (ii) (v) (b) (i) (ii) (iv) (c) All (d) None

ANS: (b)

6) System Bus is Collection of _____ lines.

(a) Address Bus (b) Control Bus (c) Data Bus (d) All

ANS: (d)

7) Address Bus is _____

(a) Uni-Directional (b) Bi-Directional

ANS: (a)

8) Data Bus is _____

(a) Uni-Directional (b) Bi-Directional

ANS: (b)



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9) System software is considered as interface between

(i) H/W (ii) S/W (iii) User Applications

(i) (ii) (b) (i) (iii) (c) (ii) (iii) (d) All

ANS: (b)

10) Program is considered as set of _____

(a) Instructions (b) Sub-Routines (c) Sub-Programs (d) Functions

ANS: (a)

11) Bit is _____

(a) Binary Data (b) Binomial Data (c) Binary Digit (d) None

ANS: (c)

12) Units of information a system can process in a given amount of time is known as _____

(a) Response (b) Throughput (c) Performance (d) None

ANS: (b)



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13) Vacuum Tubes were used for circuitry by _____ generation computers'

- (a) 4th (b) 3rd (c) 2nd (d) 1st

ANS : (d)

14) Transistors were used in _____

- (a) 4th (b) 2nd (c) 1st (d) 3rd

ANS: (b)

15) Magnetic Drum Technology is used in _____ generation computers

- (a) 1st (b) 2nd (c) 3rd (d) 4th

ANS : (a)

16) Magnetic Core Technology is used in _____ generation computers .

- (a) 1st (b) 2nd (c) 3rd (d) 4th

ANS : (b)



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17) Transistors were miniature and placed on silicon chips called _____

(a) Semi Conductors (b) Magnetic Conductors (c) Resistors (d) Semi Transistors

ANS : (a)

18) 3rd generation computers used IC

(a) True (b) False

ANS: (a)

19) Thousands of ICs are built into a single silicon chip called _____

(a) Nano Technology (b) MicroProcessor (c) VLSI (d) None

ANS : (c)

20) Stored Program Concept is in

(a) Von Neumann machine (b) Harvard machine (c) Both (d) None

ANS : (a)



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21)In Von Neumann machine instructions and data are present in_____memory.

(a)Same RAM (b) Separate RAMs (c) both (d) None

ANS : (a)

22)In Harvard machine instructions and data are present in_____memory.

(a)Same RAM (B) Separate RAM (c) Both (d) None

ANS: (b)

23)Computers can be classified into _____number of generations.

(a)10 (b) 8 (c) 5 (d) 4

ANS : (c)

24)Low Level Languages can be understood by _____

(a)Users (b) Lay Men (c) Machine (d) None

ANS : (c)



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25)Low Level Languages consists of_____

(a)Bits (b) Structures (c) Macros (d) Functions

ANS : (a)

26)Assemblers/Compilers are required to convert

(a)MLL to HLL (b) HLL to MLL (c) Both (d) None

ANS : (b)

27)_____is concerned with the structure and behavior of the computer as seen by user.

(a)Computer Architecture (b) Computer Organization (c) Computer Design

ANS : (a)

28)_____is concerned with the way the h/w components operate and the way they are connected together.

(a)Computer Design (b) Computer Architecture (c) Computer Organization

ANS :(c)



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29)In READ control signal data transfer is between _____ and _____.

(a)Memory , CPU (b) Memory , I/O (c) CPU , Memory (d) I/O , Memory

ANS : (a)

30)In WRITE control signal data transfer is between _____ and _____.

(a)Memory , CPU (b) Memory , I/O (c) CPU , Memory (d) I/O , Memory

ANS : (c)

31)If there are 8 data lines then the size of Data Bus is

(a)8 (b) 16 (c) 24 (d) None

ANS : (a)

32)If there are 8 address lines then the number of memory locations in the memory are_____

(a)8 (b) 256 (c) 128 (d) 64

ANS : (b)



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33)If there are 14 address lines and 8 data lines .then the memory capacity is_____

(a)4 Kilo Bits (b) 4K bits (c) 16 Kilo Bytes (d) 16 Kilo Bits

ANS : (a)

34)A 64 K bytes memory with 8 data lines has _____address lines

(a)24 (b) 12 (c) 16 (d) 10

ANS : (c)

35)A 4GB memory with 8 bit data lines has _____address lines

(a)32 (b) 64 (c) 22 (d) 12

ANS : (a)

36)In Secondary Storage, the information that is stored is accessed_____

(a)Frequently (b) Infrequently (c) Cannot be determined (d) None

ANS: (b)



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37) Programs must reside in memory during execution

(a) True (b) False

ANS : (a)

38) The High Speed Storage elements in the processor are called _____

(a) Multiplexers (b) Decoders (c) Encoders (d) Registers

ANS : (d)

39) _____ holds the instruction that is currently being executed.

(a) IR (b) PC (c) MAR (d) MDR

ANS : (a)

40) The management of concurrent execution of several application programs is done by the _____

(a) Compiler (b) Assembler (c) OS (d) All

ANS : (c)



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41) Which memory device is generally made of semi-conductors ?

(a) RAM (b) Hard-disk (c) Floppy disk (d) CD disk

ANS: (a)

42) The internal Components of the processor are connected by _____ .

(a) Processor intra-connectivity circuitry (b) System bus

(c) Memory bus (d) Rambus

ANS: (b)

43) During the execution of a program which gets initialized first ?

(a) MDR (b) IR (c) PC (d) MAR

ANS: (c)

ANS: (c)

44) Which of the register/s of the processor is/are connected to Memory Bus ?

(a) PC (b) MAR (c) IR (d) Both a and b

ANS: (b)



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45)The registers, ALU and the interconnection between them are collectively called as ____

- (a) Process route (b) Information trail (c) information path (d) data path

ANS: (d)

46)The main virtue for using single Bus structure is ,

- (a) Fast data transfers (b) Cost effective connectivity and speed
(c) Cost effective connectivity and ease of attaching peripheral devices (d)
None

ANS: (c)

47)The main advantage of multiple bus organisation over single bus is,

- (a) Reduction in the number of cycles for execution (b) Increase in size of the registers (c) Better Connectivity (d) None of these

ANS: (a)



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48)SPEC stands for,

- (a) Standard Performance Evaluation Code (b) System Processing Enhancing Code.
(c) System Performance Evaluation Corporation (d) Standard Processing Enhancement Corporation

ANS: (c)

49)The ultimate goal of a compiler is to,

- (a) Reduce the clock cycles for a programming task (b) Reduce the size of the object code. (c) Be versatile. (d) Be able to detect even the smallest of errors.

ANS: (a)

50)To extend the connectivity of the processor bus we use _____ .

- (a) PCI bus (b) SCSI bus (c) Controllers (d) Multiple bus

ANS: (a)